

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name. I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: LATERAL TRANSISTOR HAVING GRADED BASE REGIO
SEMICONDUCTOR INTEGRATED CIRCUIT AND, the specification of which

☒ is attached hereto. FABRICATION METHOD THEREOF
☐ was filed on _____ as U.S. Application or PCT International Application No. _____ and was amended (if applicable) on _____.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above. I do not know and do not believe that the same was ever known or used by others in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to the date of this application. I further state that the invention was not in public use or on sale in the United States of America more than one year prior to the date of this application. I understand that I have a duty of candor and good faith toward the Patent and Trademark Office, and I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code §119(a)-(d) or §365(b) of any foreign application(s) for patent or inventor's certificate, or §365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below any foreign application for patent or inventor's certificate disclosing subject matter in common with the above-identified specification and having a filing date before that of the application on which priority is claimed:

Country	App. No.	Date of Filing	Priority Claimed Under 35 USC §119
Japan	P2001-128187	April 25, 2001	Yes <input checked="" type="checkbox"/> No <input type="checkbox"/>

I hereby claim the benefit under Title 35, United States Code, § 120 of any prior United States application(s), or §365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each claim of the present application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations §1.56, which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Application No.	Filing Date	Status: patented, pending, abandoned

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statement were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patents issuing thereon.

I hereby authorize the U.S. attorneys named herein to accept and follow instructions from Miyoshi&Miyoshi, as to any action to be taken in the Patent and Trademark Office regarding this application, without direct communication between the U.S. attorney and the undersigned. In the event of a change in the persons from whom instructions may be taken, the U.S. attorney named herein will be notified by the undersigned.

POWER OF ATTORNEY: The following attorneys are hereby appointed to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: Anthony B. Askew - 24,154; Roger T. Frost - 22,176; Robert E. Richards - 29,105; Stephen M. Schaezel - 31,418; Larry A. Roberts - 31,871; James Dean Johnson - 31,771; Leona G. Young - 37,266; Jamie L. Greene - 32,467; Mary Anthony Merchant - 39,771; Brenda Ozaki Holmes - 40,339; Kimberly J. Prior - 41,483; Theodore M. Green - 41,801; John K. McDonald - 42,860; Suzanne Seavello Shope - 37,933; Sima Singadia Kulkarni - 43,732; Christopher J. Chan - 44,070; John M. Briski - 44,562; S. Craig Hemenway - 44,759; Paul E. Knowlton - 44,842; Charles E. Peeler - 45,004; Cheryl L. Huseman - 45,392; Shelby B. Grier - 45,785; Vaibhav P. Kadaba - 45,865; Donald R. Andersen - 28,280; Joseph Bennett-Paris - P47,226

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Full name of sole or first inventor:	<u>Makoto YAMAMOTO</u>
Citizenship:	<u>Japan</u>
Residence:	<u>Saitama-ken, Japan</u>
Post Office Address:	<u>c/o Sanken Electric Co., Ltd., 3-6-3, Kitano, Niiza-shi, Saitama-ken, Japan</u>
Inventor's signature	<u>Makoto Yamamoto</u> Date: <u>October 22, 2001</u>

☒ Additional inventors are being named on separately numbered sheets attached hereto.

Attorney Docket No.: 44471-265522 (13700)

Title: Lateral Transistor Having Graded Base Region, Semiconductor Integrated

Page 2 Circuit And Fabrication Method Thereof

Full name of second joint inventor, if any: Akio IWABUCHI	
Citizenship: Japan	
Residence: Saitama-ken, Japan	
Post Office Address: c/o Sanken Electric Co., Ltd., 3-6-3, Kitano, Niiza-shi, Saitama-ken, Japan	
Inventor's signature <i>Akio Iwabuchi</i>	Date: October 22, 2001

Full name of third joint inventor, if any:	
Citizenship:	
Residence:	
Post Office Address:	
Inventor's signature	Date:

ASSIGNMENT

The undersigned Inventor(s),

Makoto YAMAMOTO and Akio IWABUCHI

in consideration of One Dollar (\$1.00) and other good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, does (do) hereby sell, transfer, and assign to the Assignee,

Sanken Electric Co., Ltd.

a corporation organized and existing under the laws of Japan, and having a place of business at 3-6-3, Kitano, Niiza-shi, Saitama-ken, Japan

the full, exclusive, and entire right, title, and interest for the territory of the United States of America, in and to the invention entitled LATERAL TRANSISTOR HAVING GRADED BASE REGION, SEMICONDUCTOR INTEGRATED CIRCUIT AND FABRICATION METHOD THEREOF

as described and claimed in the application for Letters Patent executed by me (us) on the 22th day of October, 2001 and the full, exclusive and entire right, title, and interest in and to said application, in and to any divisions, continuations, reexaminations, and reissues thereof; and I(we) hereby request the Commissioner of Patents and Trademarks of the United States of America to issue said Letters Patent to the Assignee, for its interest, for the sole use of the Assignee and its successors, assigns and legal representatives, as fully and entirely as the same would have been held by me (us) had this assignment not been made;

AND, for the same consideration, I (we) agree to sign all lawful papers, execute all division, continuing, reexamination, reissue and other applications, make all assignments and rightful oaths, and generally do everything possible to the Assignee, its successors, assigns, and nominees, to obtain and enforce proper protection for all said inventions and improvements in the United States of America.

I (we) hereby authorize and request my (our) attorney, Roger T. Frost, Reg. No. 22,176, to insert here in parentheses the application number (App. No. _____) and filing date (_____) of the aforesaid application when known.

Date Signed: October 22, 2001

Makoto Yamamoto
(Inventor) Makoto YAMAMOTO

Date Signed: October 22, 2001

Akio Iwabuchi
(Inventor) Akio IWABUCHI

Date Signed: _____

(Inventor)

J&A Docket: 44-71-265522 (13700)